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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/814,057	03/22/2001	Kazutaka Shibata	ROH-034	2536

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RADER FISHMAN & GRAUER PLLC
LION BUILDING
1233 20TH STREET N.W., SUITE 501
WASHINGTON, DC 20036

EXAMINER

ROSE, KIESHA L

ART UNIT PAPER NUMBER

2822

DATE MAILED: 02/13/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/814,057

Applicant(s)

SHIBATA, KAZUTAKA

Examiner

Kiesha L. Rose

Art Unit

2822

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 09 January 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1 and 3-5 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1 and 3-5 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ 6) ☐ Other: _____

DETAILED ACTION

This Office Action is in response to the amendment filed 9 January 2003.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 1 is rejected under 35 U.S.C. 102(b) as being anticipated by Shirai et al. (U.S. Patent 6,137,687).

Shirai discloses a printed circuit board (Fig. 2 and 6A-6F) that contains a semiconductor chip (210), a wiring board (1, 201) with a thickness of 80-100 μ m electrically connected to one surface of the semiconductor chip (210), a warp preventing board (212) with a thickness of 80-100 μ m; composed of the same material as the wiring board, joined to the other surface of the semiconductor chip (210) through adhesive and an external connection member (9) for surface mounting on a surface of the wiring board (1, 201) facing away from the semiconductor chip (210).

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Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 3 and 5 are rejected under 35 U.S.C. 103(a) as being unpatentable over Shirai et al. in view of Ball (U.S. Patent 6,165,815).

Shirai discloses all of the limitations except for the warp preventing board being a wiring board and another semiconductor chip electrically connected to the warp preventing board. Whereas Ball discloses a semiconductor device (Fig. 8) that contains a wiring board (606) with a first chip (604) formed thereon with a second chip (602) formed on first chip (604) with a second wiring board (808) electrically connected to the second chip (602). The second chip is electrically connected to the top wiring board to make electrical contact with the leads or other electrical contact elements on the surface of the top wiring board. (Column 8, lines 15-22) Since Shirai and Ball are both from the same field of endeavor, semiconductor devices, the purpose disclosed by Ball would have been recognized in the pertinent art of Shirai. Therefore it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the device of Shirai by incorporating a second chip electrically connected to a top wiring board to establish electrical communications to peripheral components as taught by Ball.

Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Shirai et al. in view of Lee (U.S. Patent 6,303,997).

Shirai discloses all of the limitations except for there being a wiring material for connecting the wiring board and warp preventing board together. Whereas Lee discloses a semiconductor package (Fig. 3) that contains a plurality of printed circuit boards (10) stacked on top of one another with a wiring material (40) interposed there between for electrical connection. The wiring material is formed to be the signal input/output of the package. (Column 2, lines 64-67) Since Shirai and Lee are both from the same field of endeavor, semiconductor devices with printed circuit boards, the purpose disclosed by Lee would have been recognized in the pertinent art of Shirai. Therefore it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the printed circuit board of Shirai by incorporating a wiring layer interposed between the wiring board and the warp preventing board to serve as a signal input/output for the package as taught by Lee.

Response to Arguments

Applicant's arguments with respect to claims 1 and 3-5 have been considered but are moot in view of the new ground(s) of rejection.

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Conclusion


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kiesha L. Rose whose telephone number is 703-605-4212. The examiner can normally be reached on M-F 8:30-6:00 off 1st Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 703-308-4905. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-7722 for regular communications and 703-308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.


KLR

February 6, 2003


AMIR ZARABIAN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800